

FIG. 1
PRIOR ART

VSC INSTRUCTION

OPC	X	Y	Z
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$M (Vy (0), Vy (1), \dots, Vy (VL-1)) \leftarrow Vz$

FIG. 2
PRIOR ART

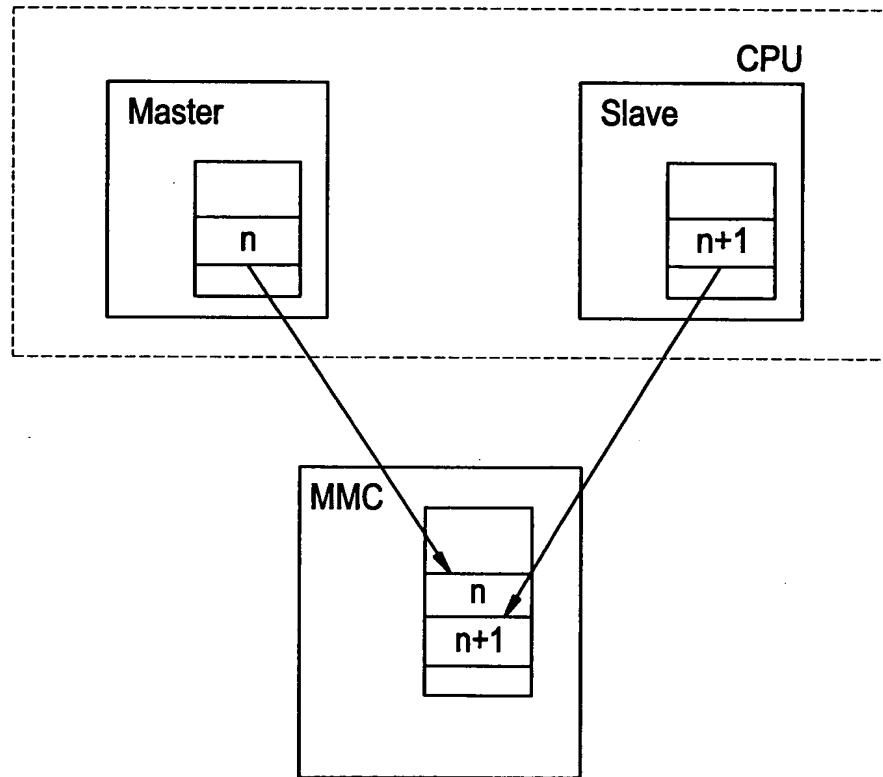


FIG. 3

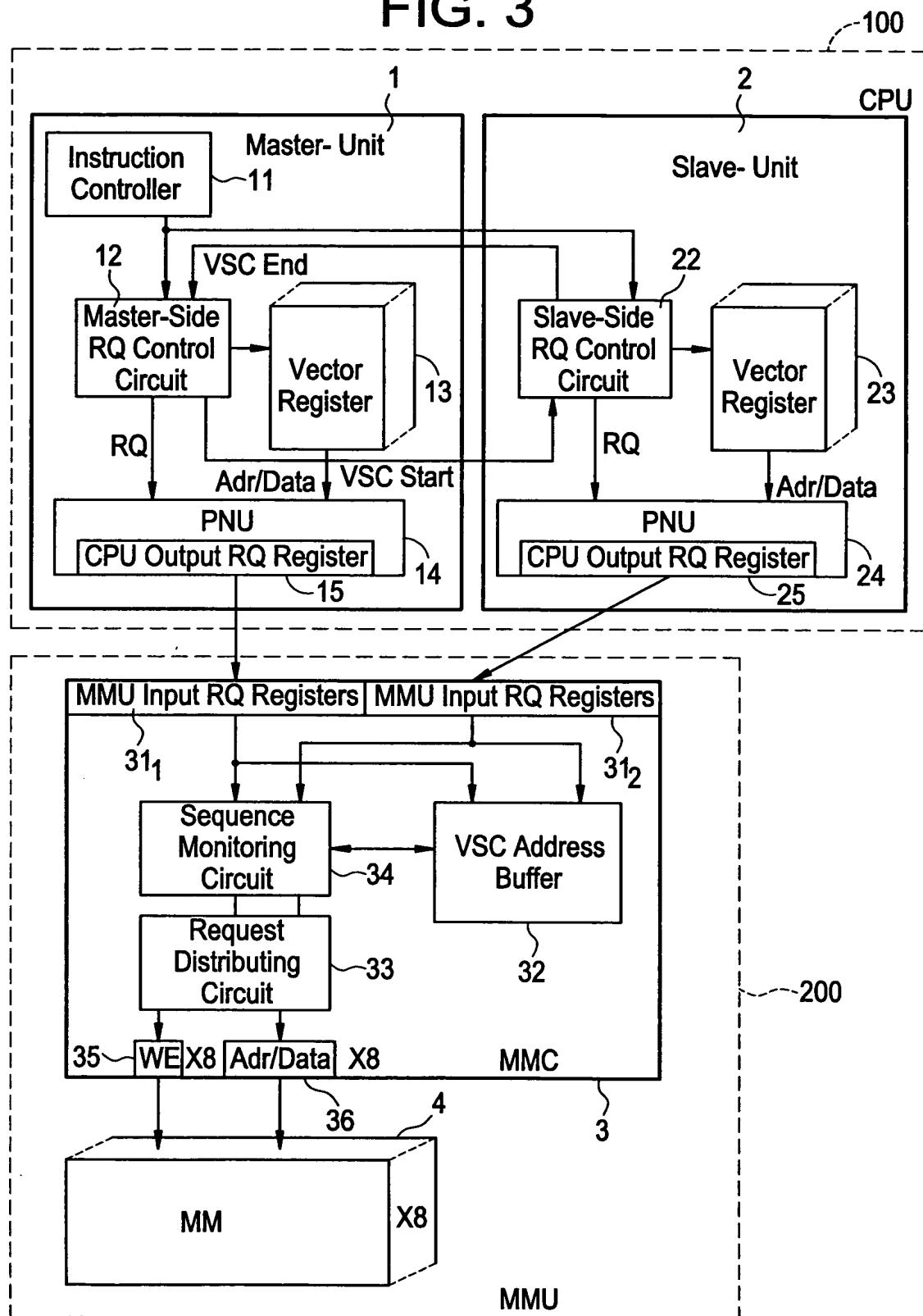


FIG. 4

From Master-Unit/Slave-Unit

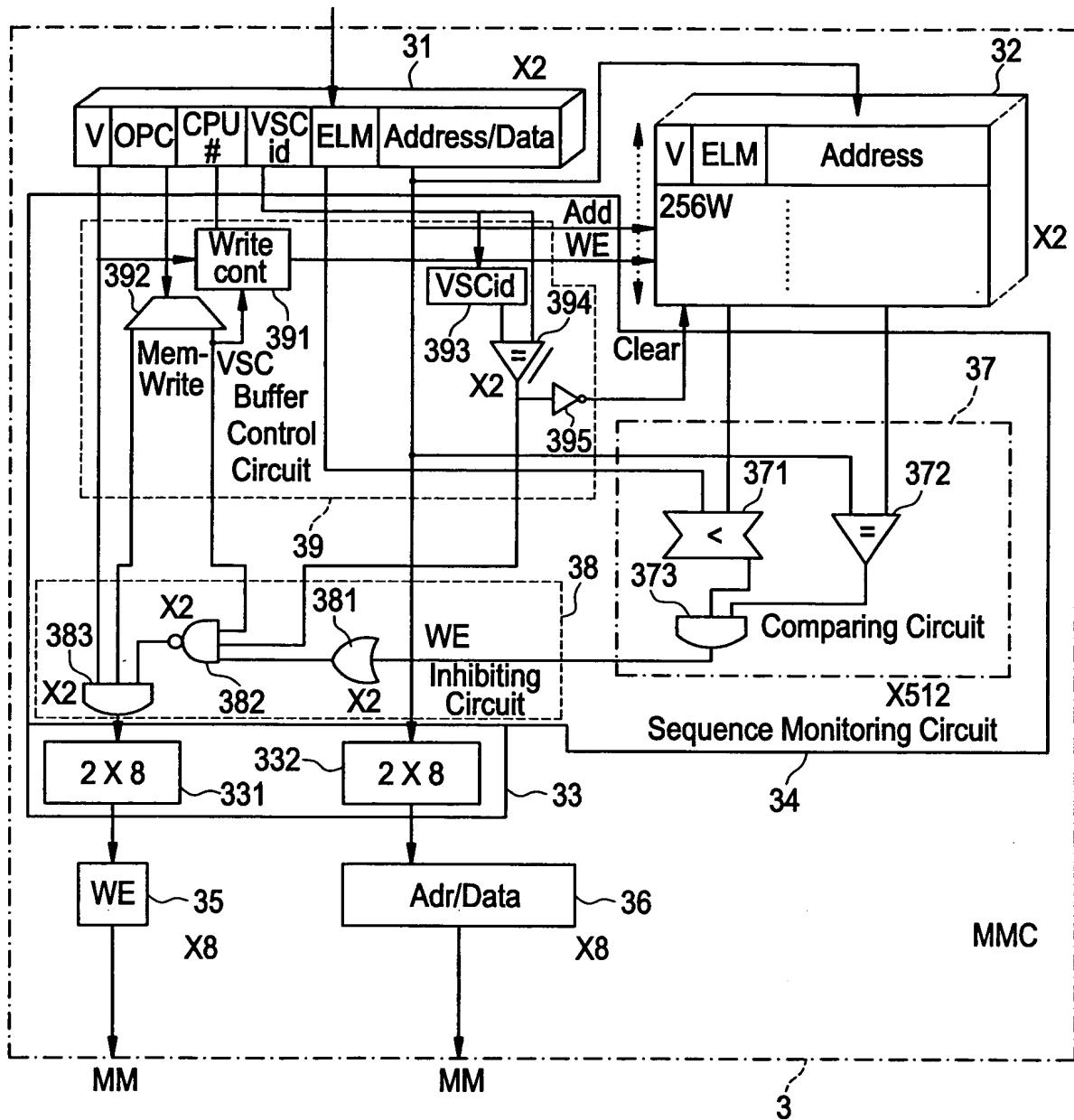


FIG. 5

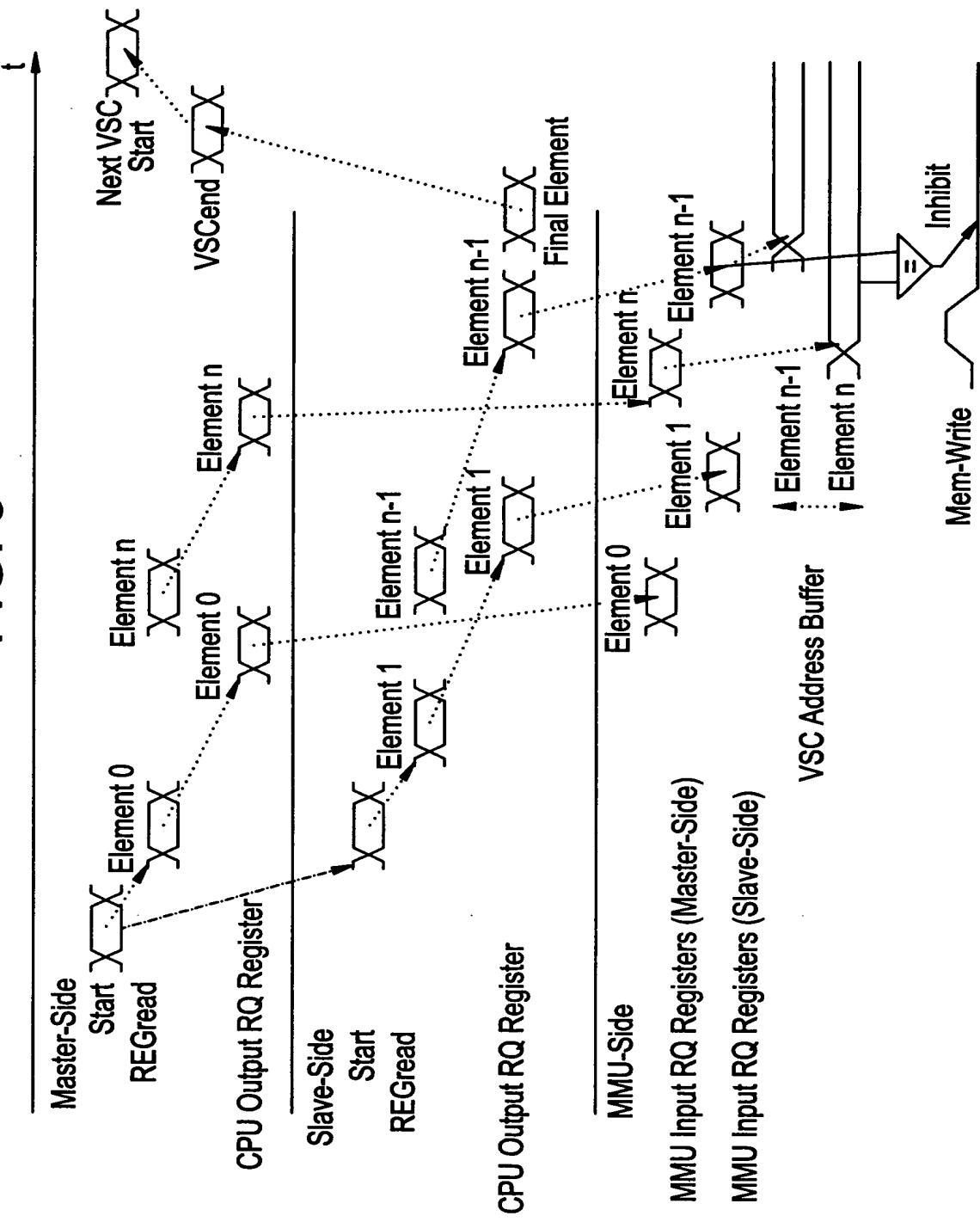


FIG. 6

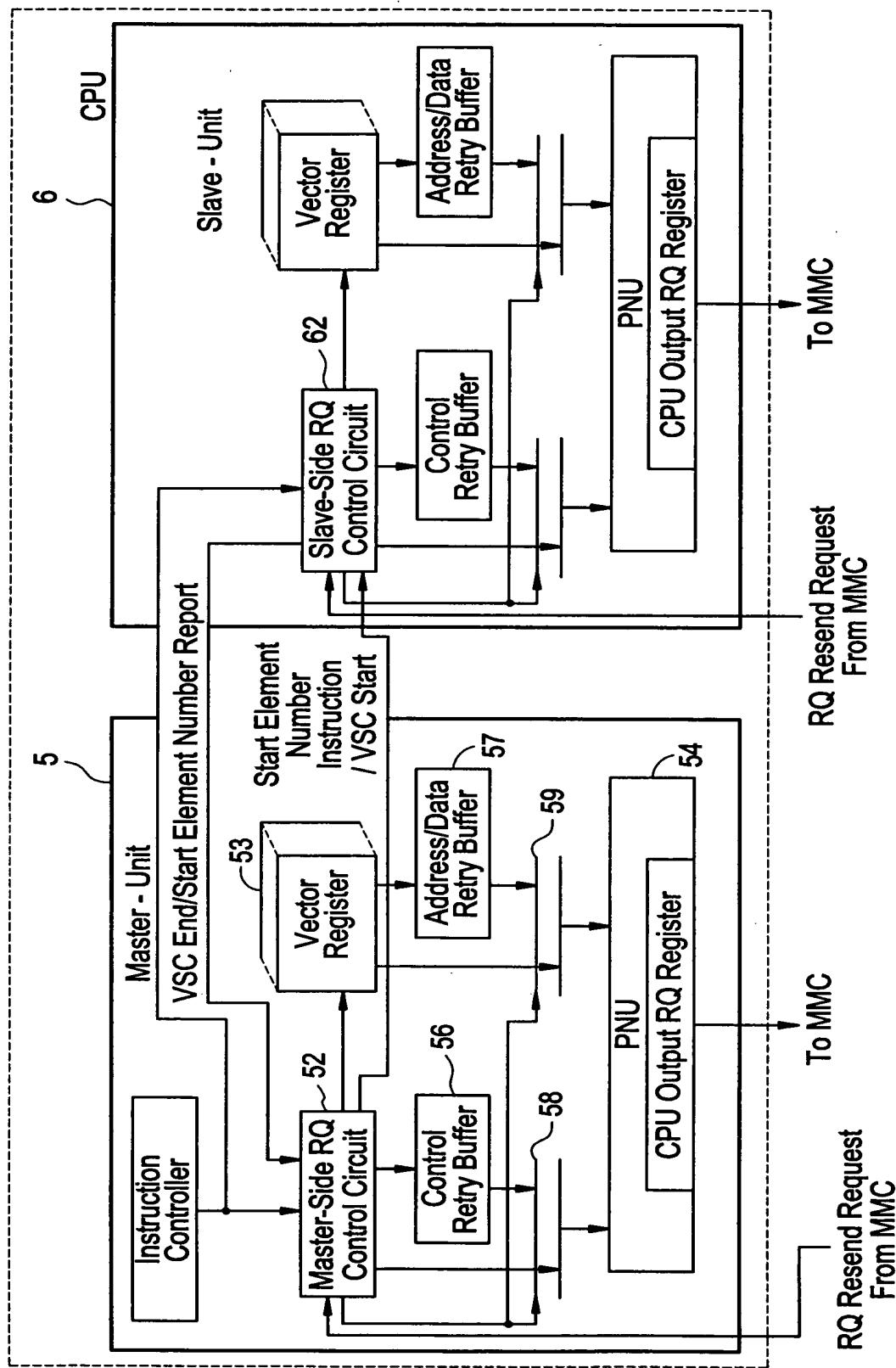
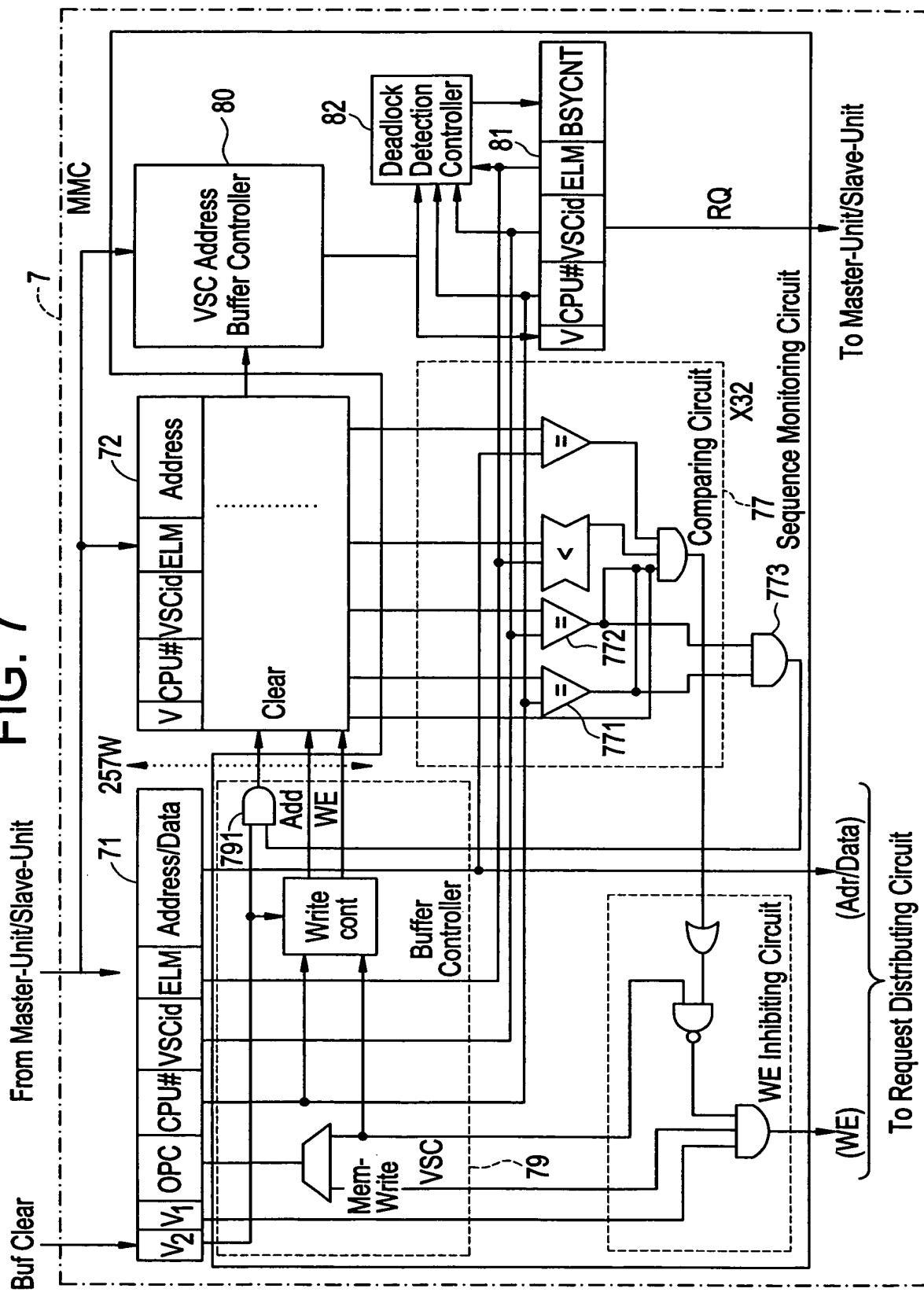


FIG. 7



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FIG. 8

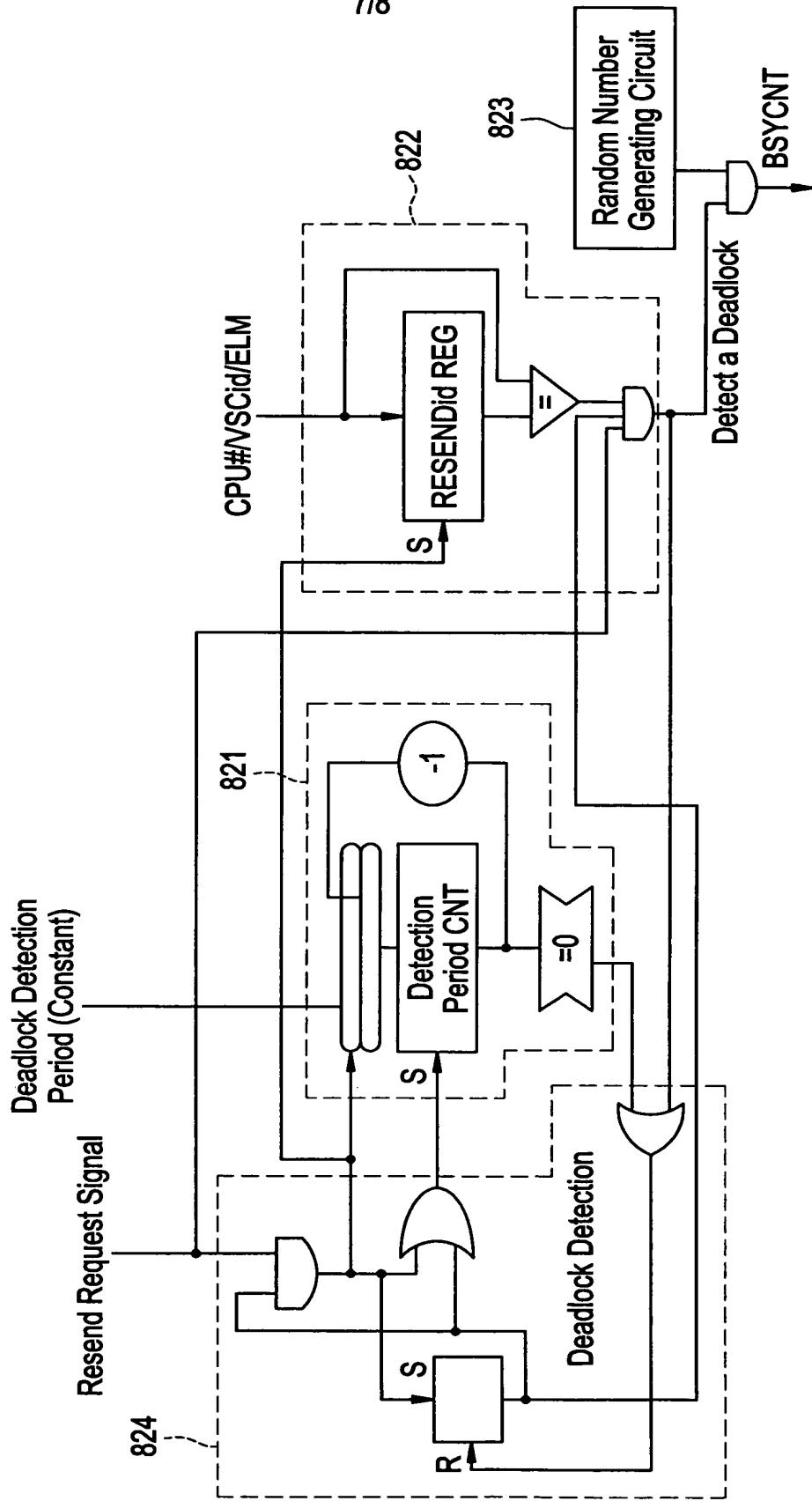


FIG. 9

